











## **Common Mistakes in Simulation**

- Inappropriate Level of Detail:

   ¬ More detail ⇒ More time ⇒ More Bugs ⇒ More CPU

   ¬ More parameters ≠ More accurate
- Unverified Models: Bugs
- Invalid Models: Model vs. reality
- Improperly Handled Initial Conditions
- Too Short Simulations: Need confidence intervals
- Poor Random Number Generators: Safer to use a well-known generator
- Improper Selection of pseudo random number seeds

Modified from: "The Art of Computer Systems Performance Analysis" Raj Jain, Wiley, 1991 Simulation

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Sample Rea	aliz	atio	on (	of a	ın l	npı	ut F	Pro	ces	SS		
Message number	1	2	3	4	5	6	7	8	9	10	11	12
Interarrival time between i+1 and i message (seconds)	2	1	3	1	1	4	2	5	1	4	2	
Length of i <sup>th</sup> message (seconds)	1	3	6	2	1	1	4	2	5	1	1	3
										Simu	ation	13













Communic Sample Realiza	ati	ON of a	S N an Ir	<b>let</b>	<b>WOI</b> Pro	rk S cess	Sim s	iula	tio	n:		
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										Simu	lation	20

E N	xample Iultiplex	e-Time (er	History of	of Time S	tep An	alysis	of Sta	tistical		
ſ	Simulated	Message	Start of	End of	Number	Number	Time	Time	]	
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- Events are Instantaneous Occurrences Which Change the State of the System
- An Event is Described by

   The time the event is to occur
   The action to take place at the event time

   The Event Calendar is a Time Ordered
- List of Events





- Entities are the objects upon which action is performed. In network simulation entities are messages.
- Attributes are characteristics which describe entities, e.g. message length or message type.











Example - Disc Multiplexer	rete	Eve	ent S	Simu	latic	on of	faS	Statis	stica	ιI		
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										Simu	lation	33

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Example - Disc Multiplexer	rete	Eve	ent S	Simu	latic	on of	faS	Statis	stica	1		
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										Simu	lation	37

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										Simu	lation	40

![](_page_20_Figure_0.jpeg)

![](_page_20_Figure_1.jpeg)

Example - Disc Multiplexer	rete	Eve	ent S	Simu	latic	on of	faS	statis	stica	1		
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										Simu	lation	43

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![](_page_21_Figure_1.jpeg)

![](_page_22_Figure_0.jpeg)

Relative Scheduli	Merits of Time Step	and Event
Approach	Advantages	Disadvantages
Time Step	Efficient for system with very frequently occurring events Efficient for regularly spaced events	Must process at each time step Error induced by fixed finite time increment Must establish rules to order events that occur in same time increment
Event Scheduling	Only process at event times No time increment to select Flexible	Significant programming effort required
		Simulation 46

![](_page_23_Figure_0.jpeg)

![](_page_23_Figure_1.jpeg)

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![](_page_34_Figure_0.jpeg)

![](_page_34_Figure_1.jpeg)

## Relative Merits of General Purpose Languages

Advantages	Disadvantages
Wide Availability	Longer programming and debugging time
Few restrictions imposed on the model	Difficult verification
User may have prior knowledge of the language	Unless object-oriented, limited ability to reuse models
Generally more computationally efficient	Model enhancement and evolution are difficult
	71 Simulation

Relative Merits of S	Special Purpose Langu	ages
Advantages	Disadvantages	
Provide built-in simulation services to reduce programming effort	Must adhere to a particular "world view" of the language	
Provide error-checking techniques superior to those provided in general purpose languages	Availability and support	
Provide a brief, direct vehicle for expressing the concepts arising in a simulation study	Cost	
Provide ability to construct user subroutines required as a part of any simulation routine	Increased computer running time	
Contain set of subroutines for common random numbers	Training required to learn the language and modeling paradigm	
Facilitate collection and display of data produced		
Facilitate model reuse		53 Simulation

Disadvantages
Tailored to a specific modeling paradigm
May be tied to a specific hardware platform
Increased execution time
Cost

### Criteria for Selecting a Computer-Aided Analysis and Design Tool

- Availability
- Cost
- Usage
- Documentation
- Ease of Learning
- Computation Efficiency
- Flexibility
- Portability
- User Interface
- Extendibility
- Memory Requirements

![](_page_37_Figure_0.jpeg)

![](_page_37_Figure_1.jpeg)

Simulation Case Stu Simulation of ATM WAN's	Simulation Case Study: Simulation of ATM WAN's								
System Parameter	Value								
TCP MTU size	9180 bytes								
TCP processing and OS overhead time									
- DEC 3000 AXP	200-300 µs								
- SGI	550 µs								
- SPARC 10	550 µs								
- SPARC 5	700µs								
TCP user send buffer size	64 kBytes								
Slow-timer period	0.5 s								
Fast-timer period	0.2 s								
Minimum RTO	1.0 s								
AAL5 SAR processing time	0.2 µs								
AAL5 cell payload size	48 Bytes								
Switch processing time	4 µs								
Switch output buffer size per VC	256 cells								
OC-3c link speed	155 Mb/s								
TAXI link speed	100 Mb/s								
DS-3 link speed	45 Mb/s								
		77 Simulation							

![](_page_38_Figure_1.jpeg)

![](_page_39_Figure_0.jpeg)

#### Comparison of Experimental and Simulation Performance Predictions

Connection	Experimental Results	Simulation Results
Baseline results: Point-to-point connections	·	
TIOCto ARL	4.2 Mb/s	7.18Mb/s
TIOC to EDC	64.2 Mb/s	65.98 Mb/s
Simultaneous traffic streams: Single source, two destinations		
TIOC to ARL	4.45 Mb/s	4.60 Mb/s
TIOC to EDC	64.36 Mb/s	61.37 Mb/s
Simultaneous traffic streams: Two sources, single destination		
ARL to TICC	2.15 Mb/s	4.87 Mb/s
EDC to TIOC	52.42 Mb/s	65.01 Mb/s
Simultaneous full duplex traffic streams		
TIOC to ARL	4.34 Mb/s	5.16 Mb/s
ARL to TICC	4.3 Mb/s	5.16 Mb/s
TIOC to EDC	22.18 Mb/s	41.80 Mb/s
EDC to TIOC	31.18 Mb/s	41.30 Mb/s

![](_page_40_Figure_0.jpeg)

![](_page_40_Figure_1.jpeg)

# Conclusions

- Simulation can be an important tool for communication network design and analysis
- Care and thought must go into construction of communication network models
- Care and thought must go into interpretation of model output