EECS 140 and 141:
Brown and Vranesic problems
3.44, 3.46, 3.48, 3.49, 3.50,
3.51 (use timing simulation)

EECS 141 only:
Brown and Vranesic problems
3.36, 3.38, 3.42 (using Figs. 3.65 and 3.66 as directed in the problems)

Puzzler #6 (mandatory for EECS 141; extra credit for EECS 140):

We discussed how LUTs are used and interconnected in FPGAs to implement any arbitrary logic function. Think about the algorithm you might use to determine where the connections are made to and between the routing channels. If you and a friend were to come up with two different algorithms, what measures would you use to determine which is better?

Staple this sheet to the front of your work, fold vertically, and write your name and homework # on the outside.

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