# <u>3.2 Terminal Characteristics</u> of Junction Diodes (pp.147-153)

A Junction Diode -

I.E., A "real" diode!

Similar to an ideal diode, its circuit symbol is:

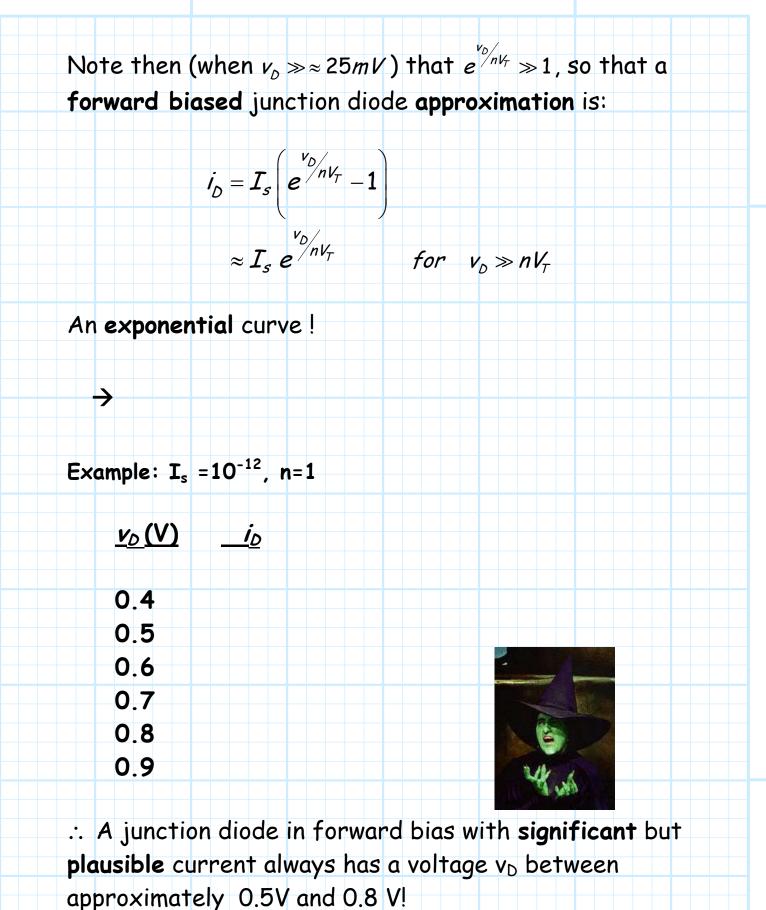
### HO: The Junction Diode Curve

HO: The Junction Diode Equation

A. The Forward Bias Region

Consider when  $v_D \gg nV_T$  (i.e, when  $v_D \gg \approx 25 mV$ ).

 $\rightarrow$ 



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#### **I.E.**, $0.5 < v_p < 0.8$ (aprox.) when in f.b.

Therfore, we often **APPROXIMATE** the **forward biased** junction diode voltage as simply:

Note that this approximation:

a)

b)

HO: The Junction Diode Forward Bias Equation

HO: Example: A Junction Diode Circuit

B. The Reverse Bias Region

Now consider when  $v_D \ll -nV_T$  (i.e, when  $v_D \ll \approx -25mV$ ).

Note then that now  $e^{\frac{v_0}{nv_T}} \ll 1$ , so that a reverse biased junction diode approximation is:

$$i_{D} = I_{S} \left( e^{v_{D}/nV_{T}} - 1 \right)$$
$$\approx -I_{S} \qquad for \quad v_{D} \ll -nV_{T}$$

Therefore, a reverse biased junction diode has a **tiny**, **negative** current.

## $\rightarrow$

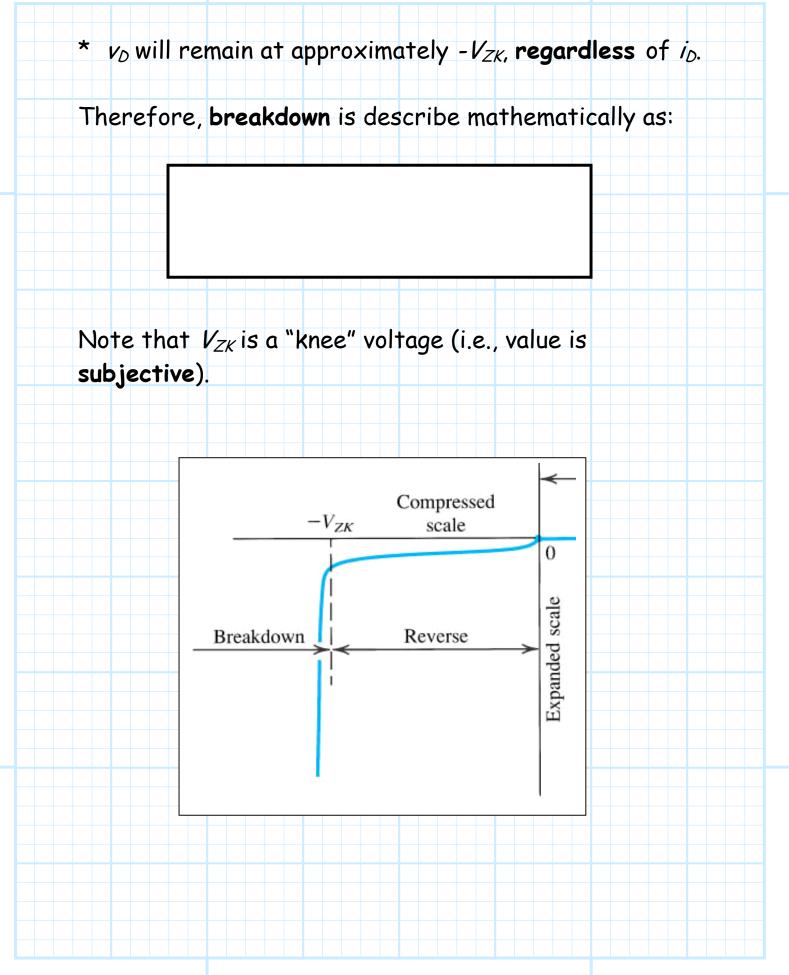
 $\rightarrow$ 

HO: Forward and Reverse Bias Approximations

### C. The Breakdown Region

If *v<sub>D</sub>* becomes too **negative**, then diode will **breakdown** (b.d.)!

\* I.E., significant current will flow from cathode to anode  $(i_D < 0 !)$ .



### D. Power Dissipation in Junction Diodes

Consider the **power** dissipated by a junction diode (i.e., P = VI)

f.b. →

 $r.b \rightarrow$ 

b.d →

Thus, we typically try to **avoid** breakdown. In other words, we desire  $V_{ZK}$  to be as **big** as possible!