

EECS 312 - Electronics Circuits I (Spring '08)

MWF 2:00-2:50

Room 2111 Learned

www.ittc.ku.edu/~jstiles/312/eeecs312.html**Instructor:** Prof. Jim Stiles**Office:** 3030 Eaton Hall 864-8803
117 Nichols Hall 864-7744**E-mail:** jstiles@eeecs.ku.edu**Office Hours:** MWF 10:00-1:45, or by appointment.**Catalog Listing:** Introduction to diodes, BJTs and MOSFETs, and their use in electronic circuits, especially digital circuits.**Prerequisite:** Upper level eligibility (EECS 220 and 140!). **Corequisite:** EECS 212.**Objective:** To provide greater proficiency for EE and CoE students in the area of electronic design and analysis, with particular emphasis on digital devices.**Required Text:** *Microelectronic Circuits*, by Sedra & Smith, **5th Edition**, ©2003**Suggested Text:** *KC's Problems and Solutions*, by K.C. Smith**Grading:** The following factors will be used to arrive at the final course grade

Homework	10 %
Quizzes	5 %
Exam I	20 %
Exam II	20 %
Exam III	20 %
Final Exam	25 %

Grading Scale: Grades will be assigned to the following scale:

A	90 - 100 %
B	80 - 89 %
C	70 - 79 %
D	60 - 69 %
F	< 60 %

These are guaranteed **maximum** scales and may be revised **downward** at the instructor's discretion.

Homework: Homework is due **before class begins** each day. **Copying is not permitted**

Exams: **No make-ups** for missed exams will be given. **If** you have attended 75% of the lectures **and** have 75% on the homework, **and** you have a legitimate excuse for missing an exam (e.g., significant illness family emergency), the first missed exam will be scored by taking 90% of the average of the other exams. Subsequent missed exams will be scored as zero. Note this policy does **not** mean that you can "drop" one exam!

Ethics Policy: Academic misconduct will **not** be tolerated. It will result in a **failing grade** and may result in further disciplinary action by the University. For details see the Academic Misconduct section of the Timetable.