EECS 312 - Electronics Circuits I (Spring '08)

MWF 2:00-2:50
Room 2111

www.ittc.ku.edu/~jstiles/312/eecs312.html

Instructor: Prof. Jim Stiles
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Office Hours: MWF 10:00-1:45, or by appointment.

Catalog Listing: Introduction to diodes, BJTs and MOSFETs, and their use in electronic circuits, especially digital circuits.

Prerequisite: Upper level eligibility (EECS 220 and 140!). Corequisite: EECS 212.

Objective: To provide greater proficiency for EE and CoE students in the area of electronic design and analysis, with particular emphasis on digital devices.


Suggested Text: KC's Problems and Solutions, by K.C. Smith

Grading: The following factors will be used to arrive at the final course grade

<table>
<thead>
<tr>
<th>Component</th>
<th>Percentage</th>
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<tbody>
<tr>
<td>Homework</td>
<td>10 %</td>
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<tr>
<td>Quizzes</td>
<td>5 %</td>
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<tr>
<td>Exam I</td>
<td>20 %</td>
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<tr>
<td>Exam II</td>
<td>20 %</td>
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<tr>
<td>Exam III</td>
<td>20 %</td>
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<tr>
<td>Final Exam</td>
<td>25 %</td>
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Grading Scale: Grades will be assigned to the following scale:

- A 90 - 100 %
- B 80 - 89 %
- C 70 - 79 %
- D 60 - 69 %
- F < 60 %

These are guaranteed maximum scales and may be revised downward at the instructor’s discretion.

Homework: Homework is due before class begins each day. Copying is not permitted.

Exams: No make-ups for missed exams will be given. If you have attended 75% of the lectures and have 75% on the homework, and you have a legitimate excuse for missing an exam (e.g., significant illness family emergency), the first missed exam will be scored by taking 90% of the average of the other exams. Subsequent missed exams will be scored as zero. Note this policy does not mean that you can “drop” one exam!

Ethics Policy: Academic misconduct will not be tolerated. It will result in a failing grade and may result in further disciplinary action by the University. For details see the Academic Misconduct section of the Timetable.