

**Special Problem 4.10-8**

The circuit below is a simple **digital inverter**.

For this inverter, **0.0 V** is the ideal "low" logic state, and **5.0 V** is the ideal "high" logic state.

Determine **approximately** the value of  $V_{OH}$  for this inverter.

**Hint:**  $V_{OH}$  is not equal to 5.0 V, although it almost is!

