Special Problem 4.10-4

A symmetric CMOS inverter has:

\[ V_{DD} = 5.0 \text{ V} \]

\[ K = 4/9 \text{ mA/V}^2 \]

\[ V_t = 0.5 \text{ V} \]

If \( v_I = v_{IL} \), what is the drain current \( i_D \)?

**Hint:** What is the drain current of the NMOS device if \( v_I = v_{IL} \)?