Example: An Inverting Network

Now let's determine the complex transfer function of this circuit:



It's the inverting configuration!

Note this circuit uses the **inverting** configuration, so that:

$$G(\omega) = -\frac{Z_2(\omega)}{Z_1(\omega)}$$

where $Z_1 = R_1$, and:

 $Z_2 = R_2 \left\| \frac{1}{j\omega C} = \frac{R_2}{1 + j\omega R_2 C} \right\|$

Therefore, the **transfer function** of this circuit is:

$$G(w) = \frac{v_{out}^{oc}(w)}{v_{in}(w)} = -\frac{R_2}{R_1} \frac{1}{1 + jwR_2C}$$

Another low-pass filter

Thus, the transfer function magnitude is:



where:



Thus, just as with the previous example, this circuit is a **low-pass filter**, with **cutoff** frequency ω_0 and pass-band **gain** $(R_2/R_1)^2$.

Example: A Non-

Inverting Network

Let's determine the transfer function $G(\omega) = v_{out}^{oc}(\omega)/v_{in}(\omega)$ for the following circuit: R_2 R_1 V_ V^{oc} out ideal R_3 V+ Vin i,=0 13 C ic



From KCL, we know:

$$i_{3}(\omega) = i_{\mathcal{C}}(\omega) + i_{+}(\omega) = i_{\mathcal{C}}(\omega) + 0 = i_{\mathcal{C}}(\omega)$$



 R_2

ideal

V_

 $v_+(w)$



we have a template!

 R_1

 R_3

The remainder of the circuit is simply the **non-inverting amplifier** that we studied earlier.

Vin



 $\boldsymbol{v}_{out}^{oc}(\omega) = \left(1 + \frac{\boldsymbol{R}_2}{\boldsymbol{R}_1}\right) \boldsymbol{v}_+(\omega)$

Combining these two relationships, we can determine the **complex transfer function** for this circuit:

$$\mathcal{G}(\omega) = \frac{v_{out}(\omega)}{v_{in}(\omega)} = \left(1 + \frac{R_2}{R_1}\right) \left(\frac{1}{1 + j\omega R_3 C}\right)$$

Jim Stiles

V^{oc} out



The magnitude of this transfer function is therefore:



 $w_0 = \frac{1}{R_3C}$

where:

This is a low-pass filter—one with pass-band gain!



Example: Another

Inverting Network

Consider now the transfer function of **this** circuit:



Some more enjoyable circuit analysis

To accomplish this analysis, we must first...

Wait! You don't need to explain this to me.

 R_3

İ3

Vin

It is obvious that we can divide this is circuit into two pieces—the first being a complex voltage divider and the second a non-inverting amplifier.

C _

V3

 i_c

 R_1

i1

 V_{3}

R2

V^{oc} out

V-

V+

Can we analyze the circuit this way?

The transfer function of the complex voltage divider is :



 $\frac{v_{out}^{oc}(\omega)}{v_3(\omega)} = -\frac{R_2}{R_1}$

and that of the inverting amplifier:





 $\frac{v_{out}^{oc}(\omega)}{v_{in}(\omega)} = \frac{v_{out}^{oc}(\omega)}{v_{3}(\omega)} \frac{v_{3}(\omega)}{v_{in}(\omega)} = -\frac{R_2}{R_1} \frac{1}{1+j\omega R_3 C}$



<u>My computer suspiciously crashed</u> <u>while writing this (really, it did!)</u>

We cannot divide this circuit into two independent pieces, we must analyze it as one circuit. R_2



<u>An even worse idea than Vista</u>

NO! This idea is as bad as the last one!

We **cannot** specify an impedance for the input network:



After all, would we define this impedance as:

$$Z_1 = \frac{v_{in} - v_{-}}{i_3}$$
 or $Z_1 = \frac{v_{in} - v_{-}}{i_1}$???
Windows Vista*







Performing some algebra, we find:

$$\mathbf{v}_{3} = \left(\frac{\mathbf{R}_{1}}{(\mathbf{R}_{1} + \mathbf{R}_{3}) + j\omega\mathbf{R}_{1}\mathbf{R}_{3}\mathbf{C}}\right)\mathbf{v}_{in}$$



This again is a low-pass filter

We can rearrange this transfer function to find that this circuit is likewise a **low-pass filter** with **pass-band gain**:

$$\mathcal{G}(\omega) = \frac{v_{out}(\omega)}{v_{in}(\omega)} = \frac{-R_2}{R_1 + R_3} \left(\frac{1}{1 + j \left(\frac{\omega}{\omega_o} \right)} \right)$$

where the cutoff frequency ω_0 is:



<u>Example: A Complex</u> <u>Processing Circuit using the</u> <u>Inverting Configuration</u>

Note that we can combine inverting amplifiers to form a more **complex** processing system.

For **example**, say we wish to take **three** input signals $v_1(t)$, $v_2(t)$, and $v_3(t)$, and process them such that the open-circuit output voltage is:

$$v_{out}(t) = 5v_1(t) + \int_{-\infty}^{t} v_2(t') dt' + \frac{d'v_3(t)}{dt}$$

Assuming that we use **ideal** (or near ideal) op-amps, with an **output resistance equal to zero** (or at least very small), we can realize the above signal processor with the following circuit:

