## <u>Example: Biasing of</u> <u>Discrete MOSFET</u> <u>Amplifiers</u>



2. Now determine the required value of  $V_{GS}$ . Since  $I_{D} = K(V_{GS} - V_{t})^{2}$ , we find that  $V_{GS}$  should be:  $V_{GS} = \sqrt{\frac{I_D}{K}} + V_{t}$  $=\sqrt{\frac{4.0}{1.0}}+1.0$ = 3.0 V 3. Set the required value of gate voltage  $V_{G}$ .  $V_{G} = V_{GS} + V_{S}$ = 3.0 + 4.0= 7.0 VSince the gate current is **zero**  $(i_{\mathcal{G}} = 0)$ , we find from voltage division that:  $V_{G} = \frac{V_{DD}}{\left(\frac{R_{I}}{R_{I}}\right) + 1}$ Therefore:  $\frac{R_1}{R_2} = \frac{V_{DD}}{V_c} - 1$  $=\frac{16.0}{7.0}-1$  $=\frac{9}{7}$ 

We need a **second** equation to explicitly determine the resistors values—the **sum** of the two resistances, for example.

We make the resistors as large a practicable. For **example**:

$$R_1 + R_2 = 240 \text{ K}$$

Therefore:

$$\frac{9}{7}R_2 + R_2 = 240$$
$$\frac{16}{7}R_2 = 240$$

and thus:

$$R_1 = 135 \text{ K}\Omega$$
 and  $R_2 = 105 \text{ K}\Omega$ 

4. Set the required value of DC drain voltage  $V_{\rho}$ .

Set the drain voltage  $V_D$  to a value half-way between  $V_{DD}$  and  $V_G - V_f!$ 

In other words, set the DC drain voltage to be:

$$V_{D} = \frac{V_{DD} + (V_{G} - V_{f})}{2}$$
$$= \frac{16 + (7.0 - 1.0)}{2}$$
$$= 11.0 \text{ V}$$

