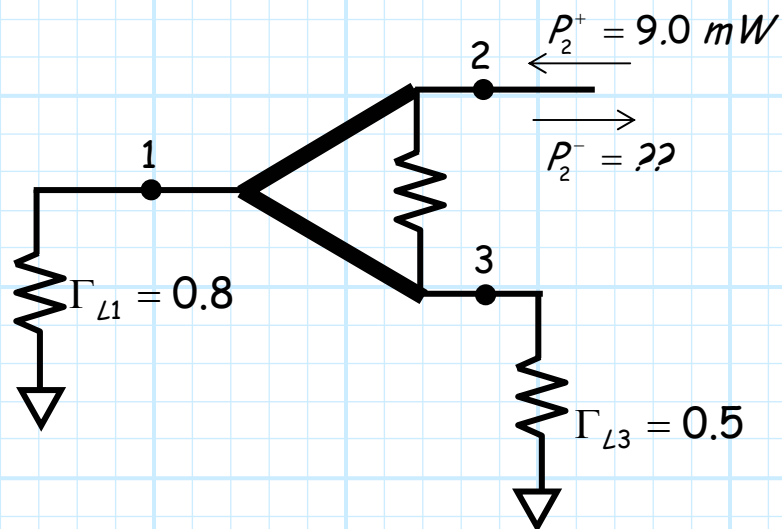


**Special Problem 7.3-4**

Consider the **Wilkinson power divider** circuit below.

A signal power of  $P_2^+ = 9.0 \text{ mW}$  is incident on port 2.

1. Using the nodes on the **following page**, draw the **signal flow graph** of this circuit.
2. **Reduce** the signal flow graph, and then determine the reflected power  $P_2^-$  from port 2.



**TURN THE PAGE !**

Carefully note the arrangement of the nodes!

$a_2$   
●

$b_1$   
●

$a_3$   
●

●  
 $b_2$

●  
 $a_1$

●  
 $b_3$